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To: Examiner: Isaac D. Stanetta	From: Leslie S. Szivos, Ph.D.
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Re: YOR920020257 US1 (15949)	CC:

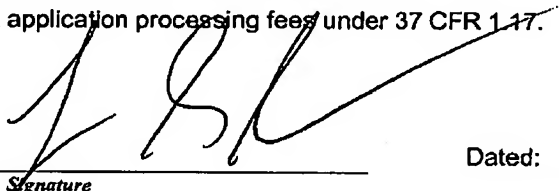
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1. Response Under 37 C.F.R. §1.111
2. Amendment Transmittal (in duplicate)
3. Certificate of Facsimile.

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AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No. YOR920020257US1 (15949)	
Applicant(s): Hussein L Hanafi, et al.					
Application No. 10/639,942	Filing Date August 13, 2003	Examiner Stanetta D. Isaac	Customer No. 23389	Group Art Unit 2812	Confirmation No. 6974
Invention: DEVICE THRESHOLD VOLTAGE CONTROL OF FRONT-GATE SILICON-ON-INSULATOR MOSFET USING A SELF-ALIGNED BACK-GATE					
<u>COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	19 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	2 -	3 =	0 x	\$86.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
<input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0510/IBM <input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.					
 _____ <i>Signature</i>			Dated: November 5, 2004		
Leslie S. Szivos, Ph.D. Registration No. 39,394			I certify that this document and fee is being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. _____ <i>Signature of Person Mailing Correspondence</i> _____ <i>Typed or Printed Name of Person Mailing Correspondence</i>		
CC:					

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**Applicant(s):** Hussein I. Hanafi, et al.**Examiner:** Stanetta D. Isaac**Serial No.:** 10/639,942**Art Unit:** 2812**Filed:** August 13, 2003**Docket:** YOR920020257US1 (15949)**For:** DEVICE THRESHOLD VOLTAGE
CONTROL OF FRONT-GATE SILICON-
ON-INSULATOR MOSFET USING A SELF-
ALIGNED BACK-GATE**Dated:** November 5, 2004**Confirmation No.** 6974Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE UNDER 37 C.F.R. § 1.111**

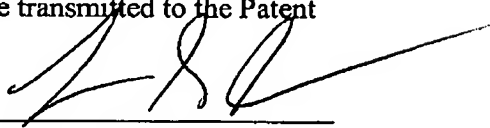
Sir:

In response to the Office Action dated August 6, 2004, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: November 5, 2004


Leslie S. Szivos, Ph.D.